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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/830,019	04/23/2004	Yoshiki Kashiwagi	042264-0101	7656

TITLE OF INVENTION: METHOD FOR DESIGNING SEMICONDUCTOR CIRCUIT DEVICE, UTILIZING BOUNDARY CELLS BETWEEN FIRST AND SECOND CIRCUITS DRIVEN BY DIFFERENT POWER SUPPLY SYSTEMS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE		
nonprovisional	NO	\$1400		\$300	\$1700	07/24/2006		
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